USFP28-LR-xx

25Gbps SFP28 Transceiver, Single Mode, Duplex LC, 10km Reach

Product Features

- Supports up to 25.78Gbps bit rates
- Hot-pluggable SFP+ footprint
- CWDM DFB laser and PIN photodiode, Up to 10km for SMF transmission
- Compliant with SFP+ MSA, IEEE802.3cc, SFF-8432, SFF-8402, and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature:

  Standard: 0 to +70°C

Applications

- 25GBASE-LR

Description

The SFP28 transceivers are high performance, cost effective modules supporting data rate of 25.78Gbps and 10km transmission distance with SMF.

The transceiver consists of three sections: a DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Tc	0		+70	°C
Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			400	mA
Data Rate			25.78		Gbps

Optical and Electrical Characteristics

Parameter		Symbol	Min	Typical	Max	Unit	Notes
Transmitter							
Centre Wavelength		λc	λc-6.5	λc	λc+6.5	nm	
Spectral Width（-20dB）		Δλ			1	nm	
Side-Mode Suppression Ratio		SMSR	30	-		dB	
Average Output Power		P_{out}	0		7	dBm	1
Extinction Ratio		ER	3.5			dB	
Data Input Swing Differential		V_{IN}	125		850	mV	2
Input Differential Impedance		Z_{IN}	90	100	110	Ω	
TX Disable	Disable		2.4		Vcc	V	
	Enable		0		0.8	V	
TX Fault	Fault		2.0		Vcc	V	

	Normal		0		0.8	V	
Receiver							
Centre Wavelength		λc	1260		1620	nm	
Receiver Sensitivity					-13.3	dBm	3
Receiver Overload					2	dBm	3
LOS De-Assert		LOS_D			-15	dBm	
LOS Assert		LOS_A	-35			dBm	
LOS Hysteresis			0.5			dB	
Data Output Swing Differential		V_{out}	185		400	mV	4
LOS		High	2.0		Vcc	V	
		Low			0.8	V	

Notes:
1. The optical power is launched into SMF.
2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS 2^{31}-1 test pattern @25.78Gps, BER ≤5×10^{-5}.
4. Internally AC-coupled.

Diagnostics

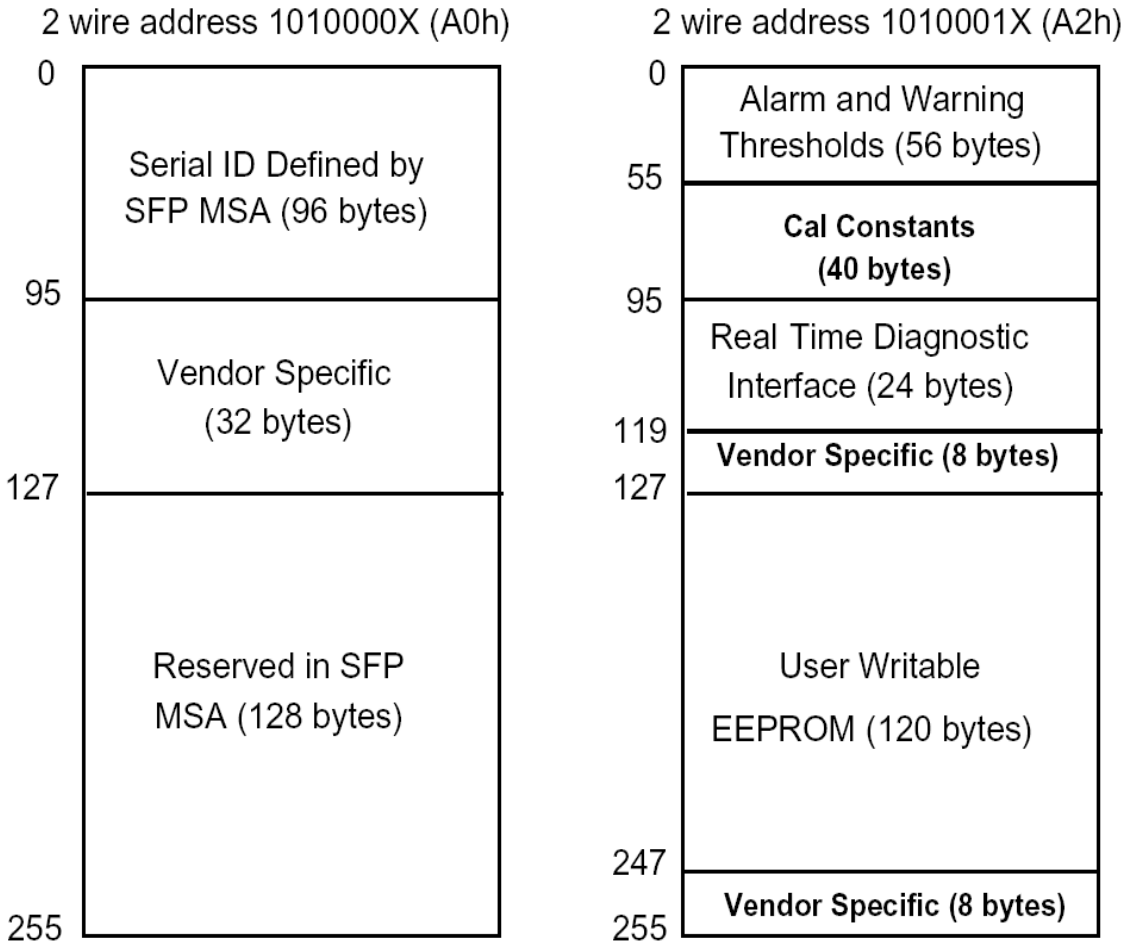
Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 100	mA	±10%	Internal
TX Power	0 to 6	dBm	±3dB	Internal
RX Power	-14 to +2	dBm	±3dB	Internal

Digital Diagnostic Memory Map

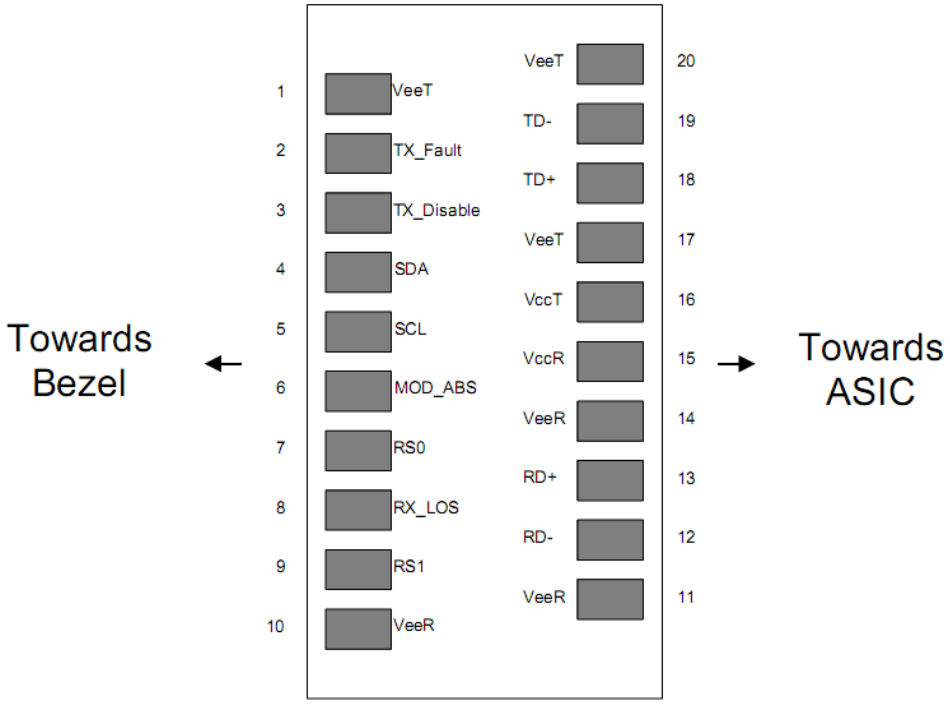
The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Pin Descriptions

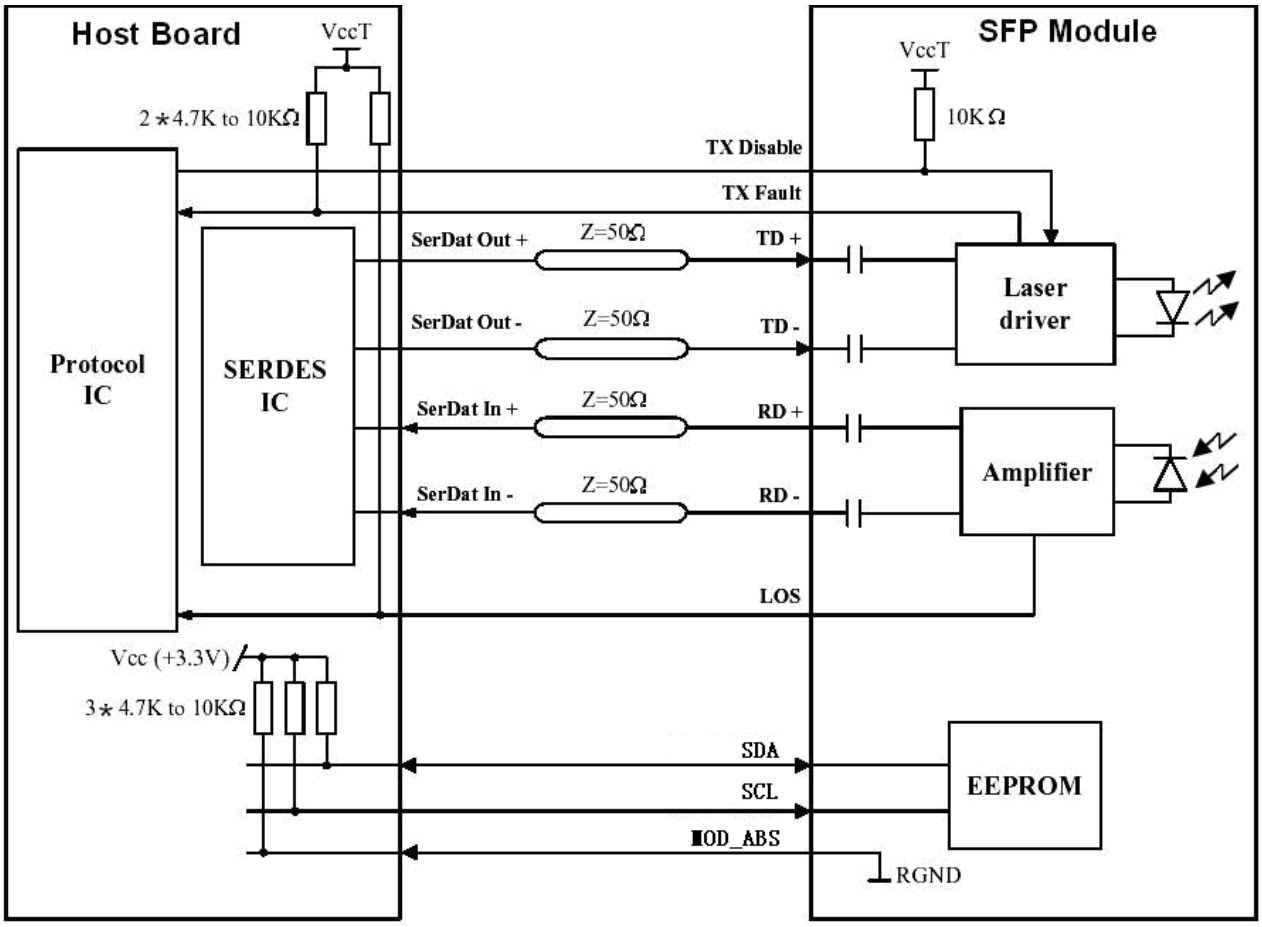


Pin	Signal Name	Description	Plug Seq.	Notes
1	V_{EET}	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V_{EER}	Receiver ground	1	
11	V_{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V_{EER}	Receiver ground	1	
15	V_{CCR}	Receiver Power Supply	2	
16	V_{CCT}	Transmitter Power Supply	2	
17	V_{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5

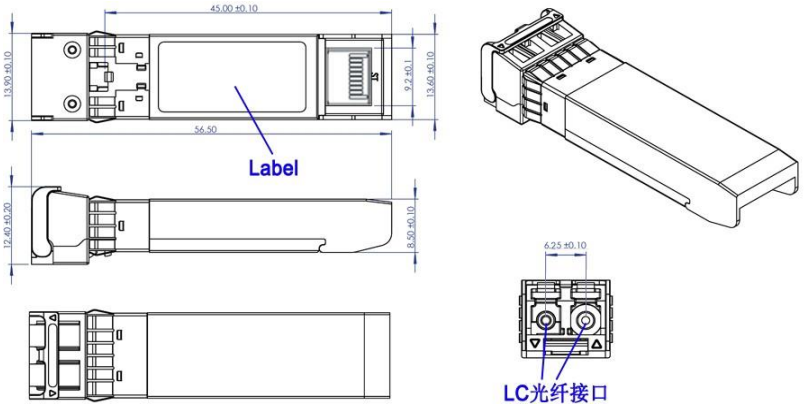
19	TD-	Inv. Transmit Data In	3	Note 5
20	V_{EET}	Transmitter Ground	1	

Notes:
Plug Seq.: Pin engagement sequence during hot plugging.
1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

Recommended Interface Circuit



Mechanical Dimensions



Ordering information

Part Number	Product Description
USFP28-LR-XX	1271~1371nm, 25.78Gbps, LC, 10km, 0°C~+70°C, with DDM

XX Wavelength Guide											
Code	**λc**	**Unit**	**Code**	**λc**	**Unit**	**Code**	**λc**	**Unit**	**Code**	**λc**	**Unit**
27	1271	nm	29	1291	nm	31	1311	nm	33	1331	nm
35	1351	nm	37	1371	nm						